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DBs US-PGPIUS, USPAT; USOCR; EPO; JPO; DERWENT

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☒ Bursts

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361/782.ccls. and (surface adj mount\$3) and trace

RRS form ISAR form Image Text HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Clas	Inventor
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020185308 A1	20021212		Depopulation of a ball grid array to allow via placement	174/260	174/250; 174/262;	361/782	Cornelius, William P. et al.
23	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020172025 A1	20021121		Structure and method for fabrication of a leadless chip carrier with embedded	361/767	174/252; 174/262;	361/782	Megahed, Mohamed et al.
24	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020089832 A1	20020711		Semiconductor package with flash-proof device	361/760	257/724; 257/787;	361/782	Huang, Chien-Ping
25	<input type="checkbox"/>	<input type="checkbox"/>	US 7224052 B2	20070529	38	IC card with controller and memory chips	257/679	257/685; 257/686;	361/782	Nishizawa; Hiroataka et al.
26	<input type="checkbox"/>	<input type="checkbox"/>	US 7133294 B2	20061107	29	Integrated circuit packages with sandwiched capacitors	361/782	257/691; 257/724;	361/782	Patel, Priyavadan R. et al.
27	<input type="checkbox"/>	<input type="checkbox"/>	US 7102892 B2	20080905	29	Modular integrated circuit chip carrier	361/770	257/686; 257/724;	361/782	Kledzik; Kenneth J. et al.
28	<input type="checkbox"/>	<input type="checkbox"/>	US 6900991 B2	20050531	29	Electronic assembly with sandwiched capacitors and methods of manufacture	361/782	257/724; 257/E21.511;	361/782	Patel, Priyavadan R. et al.
29	<input type="checkbox"/>	<input type="checkbox"/>	US 6900529 B2	20050531	13	Electronic module having a three dimensional array of carrier-mounted	257/686	257/675; 257/707;	361/782	Kledzik; Kenneth J. et al.
30	<input type="checkbox"/>	<input type="checkbox"/>	US 6924427 B2	20041228	14	Method for depopulation of a ball grid	20/940	174/260	361/782	Cornelius, William P. et al.